

FIG. 1

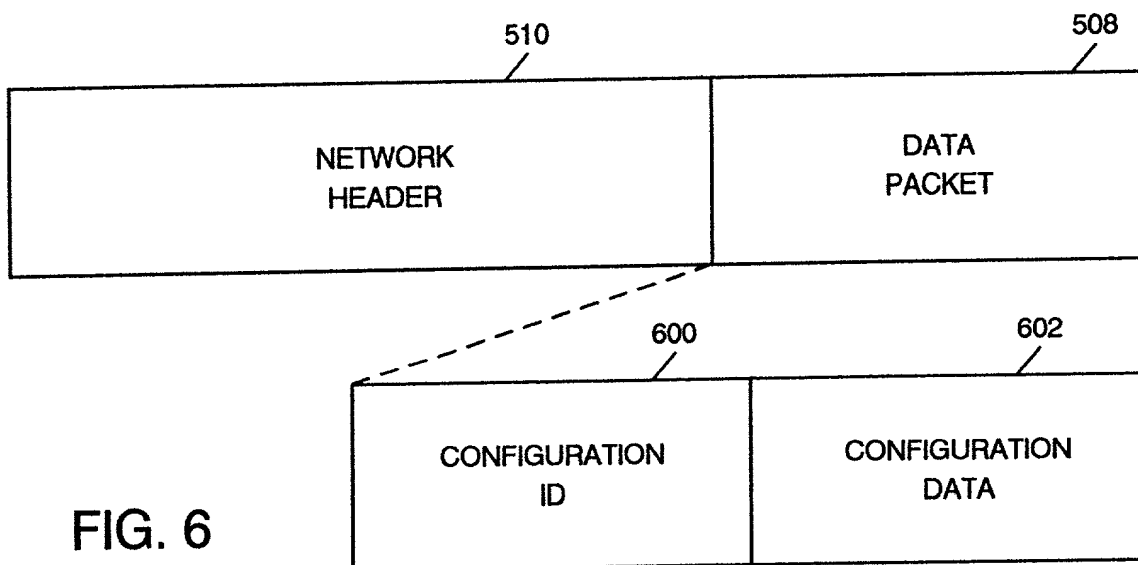
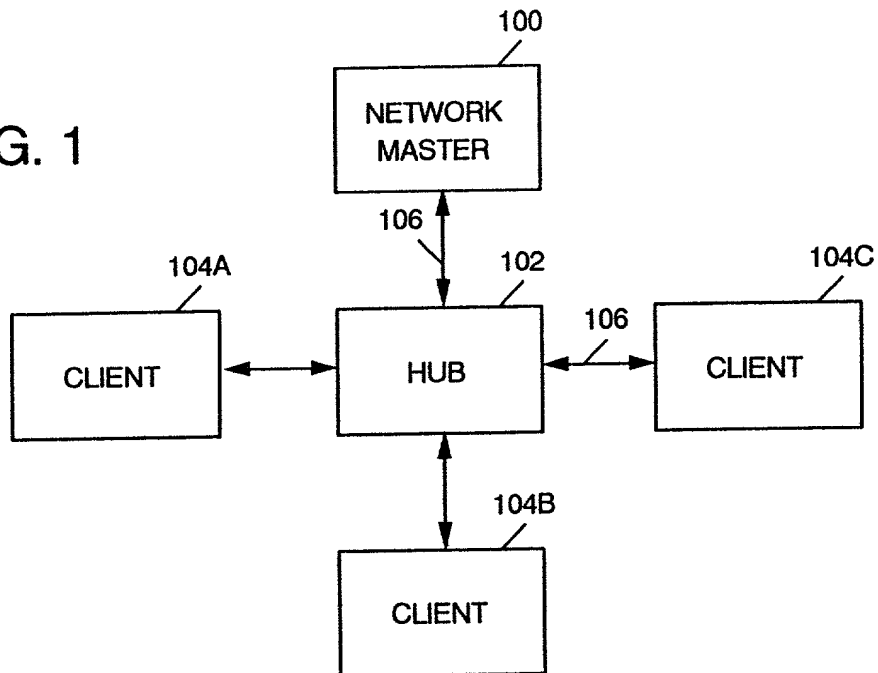


FIG. 6

OLD SERVER IP - 4 BYTES
OLD SERVER MAC - 6 BYTES
OLD UDP PORT - 2 BYTES

SYSTEM KEY - 16 BYTES
GROUP KEY - 16 BYTES
NEW SERVER IP - 4 BYTES
NEW SERVER MAC - 6 BYTES
NEW UDP PORT - 2 BYTES

FIG. 2

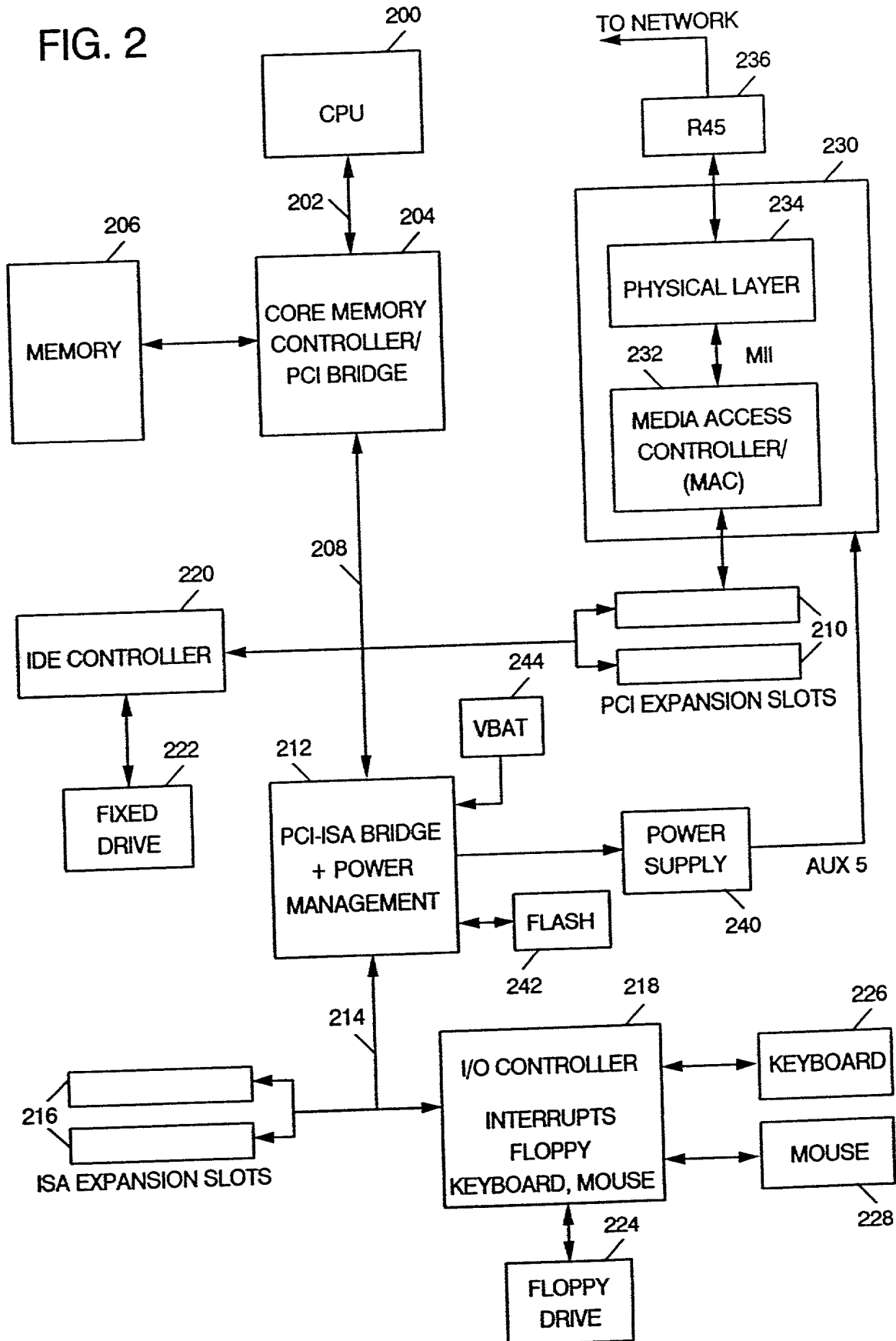


FIG. 3

The diagram illustrates a system architecture with the following components and connections:

- CPU (200)**: Connected to the **CORE MEMORY CONTROLLER/PCI BRIDGE (204)** via a bidirectional bus (202).
- MEMORY (206)**: Connected to the **CORE MEMORY CONTROLLER/PCI BRIDGE (204)** via a bidirectional bus (204).
- CORE MEMORY CONTROLLER/PCI BRIDGE (204)**: Acts as a central hub, connecting to the **IDE CONTROLLER (220)**, **PCH/ISA BRIDGE + POWER MANAGEMENT (212)**, and the **MGMT ASIC (300)**.
- IDE CONTROLLER (220)**: Connected to the **FIXED DRIVE (222)** via a bidirectional bus (222).
- PCH/ISA BRIDGE + POWER MANAGEMENT (212)**: Connected to the **IDE CONTROLLER (220)**, **MGMT ASIC (300)**, **FLASH (242)**, and **ISA EXPANSION SLOTS (216)**. It also receives power from the **POWER SUPPLY (240)** via **AUX 5**.
- MGMT ASIC (300)**: Part of a larger block (231) that includes the **PHYSICAL LAYER (234)** and **MEDIA ACCESS CONTROLLER/(MAC) (232)**. It is connected to the **CORE MEMORY CONTROLLER/PCI BRIDGE (204)** via bus (208), **EEPROM (302)** via bus (310), and **PCI EXPANSION SLOTS (210)** via bus (244). The **PHYSICAL LAYER (234)** is connected to the **TO NETWORK** via **R45 (236)**.
- PHYSICAL LAYER (234)**: Connected to the **MEDIA ACCESS CONTROLLER/(MAC) (232)** via a bidirectional bus (MII).
- MEDIA ACCESS CONTROLLER/(MAC) (232)**: Connected to the **PHYSICAL LAYER (234)** and **PCI EXPANSION SLOTS (210)**.
- EEPROM (302)**: Connected to the **MGMT ASIC (300)** via bus (310).
- PCI EXPANSION SLOTS (210)**: Connected to the **MGMT ASIC (300)** via bus (244).
- POWER SUPPLY (240)**: Provides power to the **PCH/ISA BRIDGE + POWER MANAGEMENT (212)** and **FLASH (242)**.
- FLASH (242)**: Connected to the **PCH/ISA BRIDGE + POWER MANAGEMENT (212)** via bus (242).
- ISA EXPANSION SLOTS (216)**: Connected to the **PCH/ISA BRIDGE + POWER MANAGEMENT (212)** via bus (214).
- I/O CONTROLLER (218)**: Connected to the **PCH/ISA BRIDGE + POWER MANAGEMENT (212)** via bus (214). It manages **INTERRUPTS**, **FLOPPY**, **KEYBOARD**, and **MOUSE**.
- KEYBOARD (226)**: Connected to the **I/O CONTROLLER (218)** via bus (226).
- MOUSE (228)**: Connected to the **I/O CONTROLLER (218)** via bus (228).
- FLOPPY DRIVE (224)**: Connected to the **I/O CONTROLLER (218)** via bus (224).

216

FIG. 4

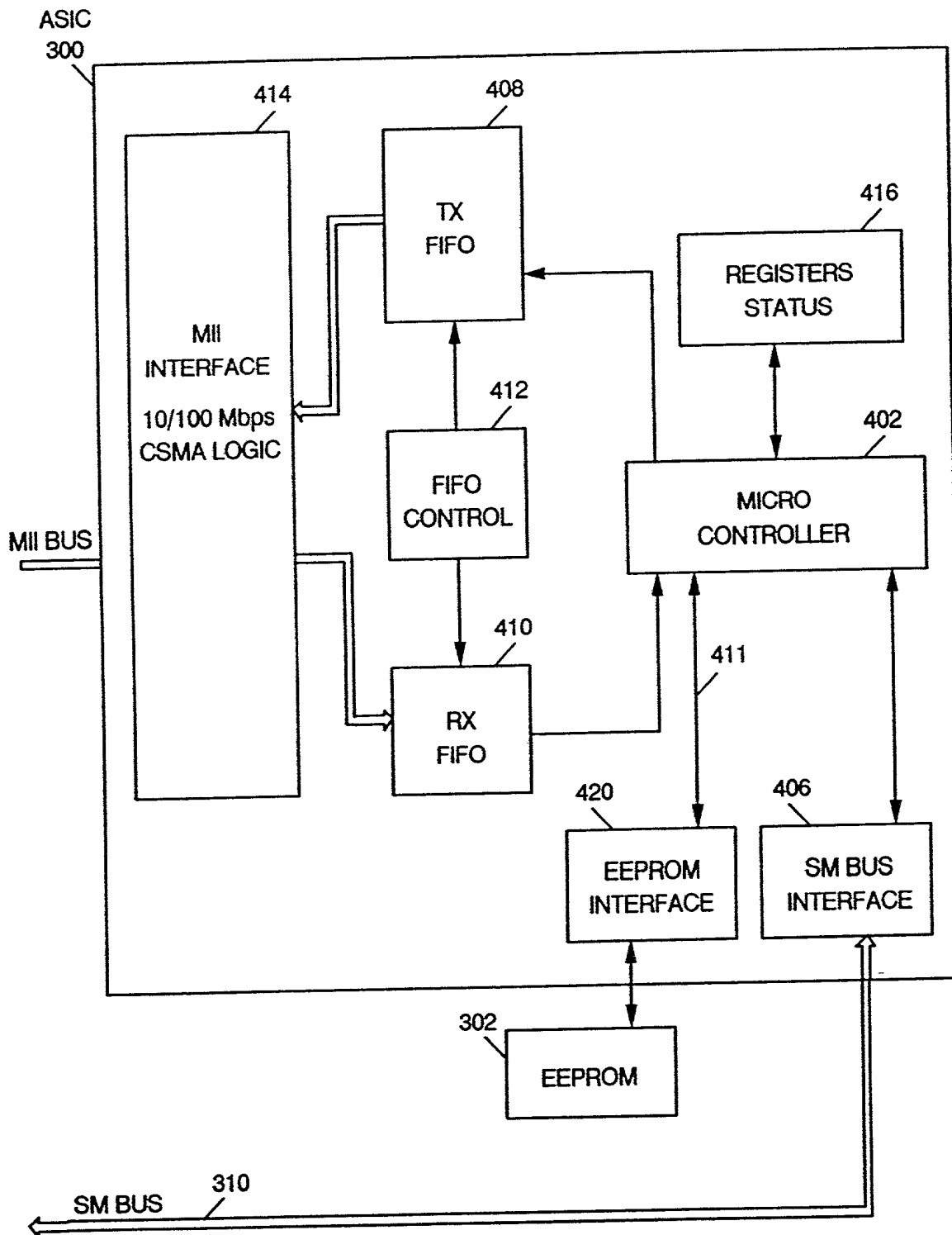


FIG. 5

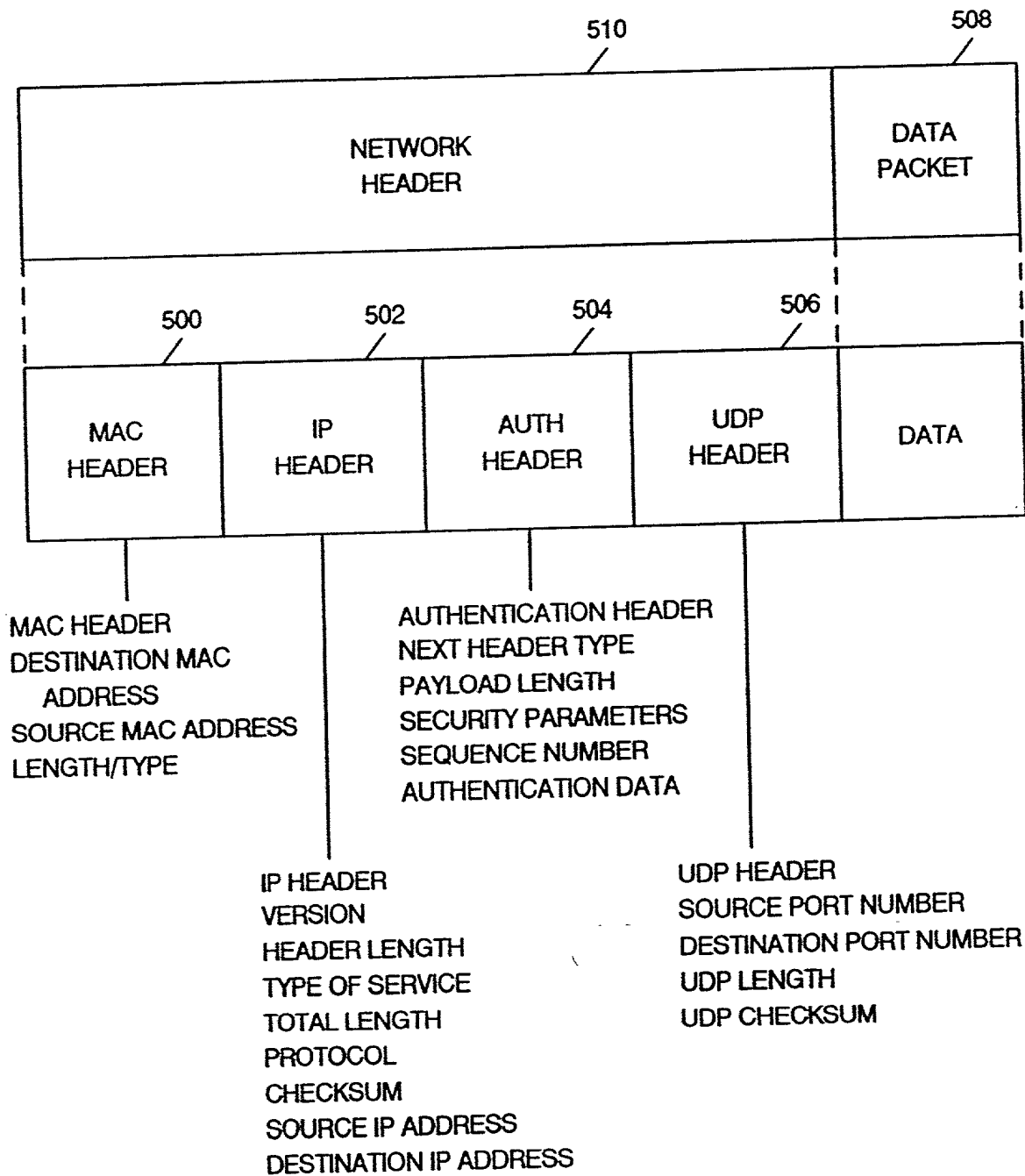


FIG. 7

